

WEST Search History

DATE: Thursday, May 12, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L38	l29 and L37	3
<input type="checkbox"/>	L37	sawase.in. and noguchi.in.	3
<input type="checkbox"/>	L36	l27 and l31	2
<input type="checkbox"/>	L35	l27 and l29	1
<input type="checkbox"/>	L34	l27 and l31L33	0
<input type="checkbox"/>	L33	l27 and l29L32	0
<input type="checkbox"/>	L32	l28 and L31	0
<input type="checkbox"/>	L31	instruction sets	5292
<input type="checkbox"/>	L30	l28 and L29	0
	<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L29	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro-program\$1 or microprogram\$1	15187
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L28	fpga and L27	22
<input type="checkbox"/>	L27	l21 or l22 or l23 or l25 or L26	51
<input type="checkbox"/>	L26	(6023742 or 6067615 or 6085317 or 6094065 or 6107824 or 6107827 or 6184705 or 6191608 or 6212639 or 6236245 or 6259286 or 6282627 or 6298366).pn.	13
<input type="checkbox"/>	L25	(5901295 or 5911082 or 5935230 or 5936424 or 5942913 or 5963050 or 5982195 or 6020757 or 6020758).pn.	9
<input type="checkbox"/>	L24	(5748912 or 5774684 or 5784637 or 5818255 or 5825202 or 5834947 or 5844424 or 5847580 or 5880597 or 5883525).pn.	10
<input type="checkbox"/>	L23	(5432719 or 5432950 or 5448493 or 5488316 or 5504439 or 5592102 or 5635881 or 5682107 or 5710891).pn.	9
<input type="checkbox"/>	L22	(4314353 or 4789951 or 4870302 or 5093915 or 5140193 or 5206940 or 5343406 or 5347641 or 5369314 or 5402014).pn.	10
<input type="checkbox"/>	L21	freeman.in. and fpga	10
<input type="checkbox"/>	L20	re03363.pn.	1
<input type="checkbox"/>	L19	5940626.uref.	5
<input type="checkbox"/>	L18	5638525.pn.	1
<input type="checkbox"/>	L17	5940626.pn.	1
<input type="checkbox"/>	L16	((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1))).ti.	5

<input type="checkbox"/>	L15	(L6 or programmable logic or pld) same L3 same L4	43
<input type="checkbox"/>	L14	5794062.uref.	29
<input type="checkbox"/>	L13	5794062.pn.	1
<input type="checkbox"/>	L12	423560.ap.	4
<input type="checkbox"/>	L11	6021186.uref.	3
<input type="checkbox"/>	L10	L8 and L9	1
<input type="checkbox"/>	L9	L7 same L6	3
<input type="checkbox"/>	L8	((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1))).ti,ab,clm.	156
<input type="checkbox"/>	L7	((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1)))	963
<input type="checkbox"/>	L6	(fpga or dpga or pal)	34773
<input type="checkbox"/>	L5	fpga and L3 and L4	9

DB=USPT,PGPB; PLUR=NO; OP=ADJ

<input type="checkbox"/>	L4	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro-program\$1 or microprogram\$1	15187
<input type="checkbox"/>	L3	macroinstruction\$1 or macro-instruction\$1 or macrocode or macro-code or macro-operation\$1 or macrooperation\$1 or macro-op\$1 or macroop\$1 or macro-program\$1 or macroprogram\$1 or cisc instruction	1472

DB=PGPB,USPT; PLUR=NO; OP=ADJ

<input type="checkbox"/>	L2	(microcode or microinstruction\$1 or micro-code or micro-instruction\$1) and L1	10
<input type="checkbox"/>	L1	fpga and ((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1)))	31

END OF SEARCH HISTORY